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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Zhongze Wang et al.

Title: METHOD TO REDUCE TRANSISTOR CHANNEL LENGTH USING SDOX

Docket No.: 303.747US1

Serial No.: 09/810,005

Filed: March 16, 2001

Due Date: August 23, 2002

Examiner: Samuel A. Gebremariam

Group Art Unit: 2811


Commissioner for Patents
Washington, D.C. 20231

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ A return postcard.
- ☒ An Amendment and Response (13 Pages).
- ☒ Clean Version of Amended Specification Paragraph (1 pg.).
- ☒ Clean Version of Pending Claims (11 pgs.).

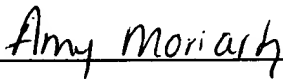
Please consider this a **PETITION FOR EXTENSION OF TIME** for sufficient number of months to enter these papers and please charge any additional required fees or credit overpayment to Deposit Account No. 19-0743.

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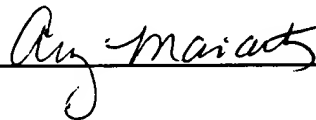
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on this 23rd day of August, 2002.

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